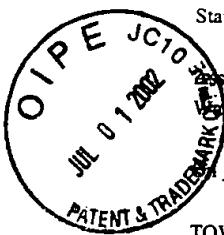


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Attorney Docket No.: 000939-073311US

Assistant Commissioner for Patents
Washington, D.C. 20231

June 24, 2002

TOWNSEND and TOWNSEND and CREW LLP

By:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Hong Koo Kim

Application No.: 09/747,779

Filed: December 22, 2000

For: FABRICATION METHOD AND
STRUCTURE FOR FERROELECTRIC
NONVOLATILE MEMORY FIELD
EFFECT TRANSISTOR

Examiner: Marcos D. Pizarro Crespo

Art Unit: 2814

**RESPONSE TO RESTRICTION
REQUIREMENT**

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Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action mailed May 29, 2002, Applicant hereby elects claims 1-24 of Group I for prosecution.

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Hong Koo Kim
Application No.: 09/747,779
Page 2

PATENT

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,



Chun-Pok Leung
Reg. No. 41,405

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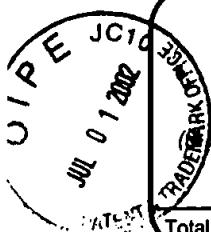
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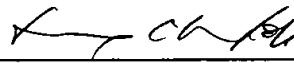
Application Number	09/747,779
Filing Date	December 22, 2000
First Named Inventor	Kim, Hong Koo
Group Art Unit	2814
Examiner Name	Marcos D. Pizarro Crespo
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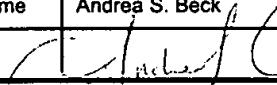
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